

Jetson Orin Nano Function	Jetson TX2 NX Function	Core 3588E Function	PIN NUMBER	PIN NUMBER	Core 3588E Function	Jetson TX2 NX Function	Jetson Orin Nano Function
GND	GND	GND_1	1	2	GND_2	GND	GND
CSI1_D0_N	CSI1_D0_N	CSI1_D0_N	3	4	CSI0_D0_N	CSI0_D0_N	CSI0_D0_N
CSI1_D0_P	CSI1_D0_P	CSI1_D0_P	5	6	CSI0_D0_P	CSI0_D0_P	CSI0_D0_P
GND	GND	GND_3	7	8	GND_4	GND	GND
CSI1_CLK_N	CSI1_CLK_N	CSI1_CLK_N	9	10	CSI0_CLK_N	CSI0_CLK_N	CSI0_CLK_N
CSI1_CLK_P	CSI1_CLK_P	CSI1_CLK_P	11	12	CSI0_CLK_P	CSI0_CLK_P	CSI0_CLK_P
GND	GND	GND_5	13	14	GND_6	GND	GND
CSI1_D1_N	CSI1_D1_N	CSI1_D1_N	15	16	CSI0_D1_N	CSI0_D1_N	CSI0_D1_N
CSI1_D1_P	CSI1_D1_P	CSI1_D1_P	17	18	CSI0_D1_P	CSI0_D1_P	CSI0_D1_P
GND	GND	GND_7	19	20	GND_8	GND	GND
CSI3_D0_N	CSI3_D0_N	CSI3_D0_N	21	22	CSI2_D0_N	CSI2_D0_N	CSI2_D0_N
CSI3_D0_P	CSI3_D0_P	CSI3_D0_P	23	24	CSI2_D0_P	CSI2_D0_P	CSI2_D0_P
GND	GND	GND_9	25	26	GND_10	GND	GND
CSI3_CLK_N	CSI3_CLK_N	CSI3_CLK_N	27	28	CSI2_CLK_N	CSI2_CLK_N	CSI2_CLK_N
CSI3_CLK_P	CSI3_CLK_P	CSI3_CLK_P	29	30	CSI2_CLK_P	CSI2_CLK_P	CSI2_CLK_P
GND	GND	GND_11	31	32	GND_12	GND	GND
CSI3_D1_N	CSI3_D1_N	CSI3_D1_N	33	34	CSI2_D1_N	CSI2_D1_N	CSI2_D1_N
CSI3_D1_P	CSI3_D1_P	CSI3_D1_P	35	36	CSI2_D1_P	CSI2_D1_P	CSI2_D1_P
GND	GND	GND_13	37	38	GND_14	GND	GND
USBSS1_RX_N	DP0_TXD0_N	DP0_TXD0_N	39	40	CSI4_D2_N	CSI4_D2_N	PCIE2_RX0_N
USBSS1_RX_P	DP0_TXD0_P	DP0_TXD0_P	41	42	CSI4_D2_P	CSI4_D2_P	PCIE2_RX0_P
GND	GND	GND_15	43	44	GND_16	GND	GND
USBSS1_TX_N	DP0_TXD1_N	DP0_TXD1_N	45	46	CSI4_D0_N	CSI4_D0_N	PCIE2_TX0_N
USBSS1_TX_P	DP0_TXD1_P	DP0_TXD1_P	47	48	CSI4_D0_P	CSI4_D0_P	PCIE2_TX0_P
GND	GND	GND_17	49	50	GND_18	GND	GND
USBSS2_RX_N	DP0_TXD2_N	DP0_TXD2_N	51	52	CSI4_CLK_N	CSI4_CLK_N	PCIE2_CLK_N
USBSS2_RX_P	DP0_TXD2_P	DP0_TXD2_P	53	54	CSI4_CLK_P	CSI4_CLK_P	PCIE2_CLK_P
GND	GND	GND_19	55	56	GND_20	GND	GND
USBSS2_TX_N	DP0_TXD3_N	DP0_TXD3_N	57	58	CSI4_D1_N	CSI4_D1_N	PCIE2_RX1_N (PCIE3_RX0_N)
USBSS2_TX_P	DP0_TXD3_P	DP0_TXD3_P	59	60	CSI4_D1_P	CSI4_D1_P	PCIE2_RX1_P (PCIE3_RX0_P)
GND	GND	GND_21	61	62	GND_22	GND	GND
DP1_TXD0_N	DP1_TXD0_N	DP1_TXD0_N	63	64	CSI4_D3_N	CSI4_D3_N	PCIE2_TX1_N (PCIE3_TX0_N)
DP1_TXD0_P	DP1_TXD0_P	DP1_TXD0_P	65	66	CSI4_D3_P	CSI4_D3_P	PCIE2_TX1_P (PCIE3_TX0_P)
GND	GND	GND_23	67	68	GND_24	GND	GND
DP1_TXD1_N	DP1_TXD1_N	DP1_TXD1_N	69	70	DSI_D0_N	DSI_D0_N	RSVD
DP1_TXD1_P	DP1_TXD1_P	DP1_TXD1_P	71	72	DSI_D0_P	DSI_D0_P	RSVD
GND	GND	GND_25	73	74	GND_26	GND	GND
DP1_TXD2_N	DP1_TXD2_N	DP1_TXD2_N	75	76	DSI_CLK_N	DSI_CLK_N	RSVD
DP1_TXD2_P	DP1_TXD2_P	DP1_TXD2_P	77	78	DSI_CLK_P	DSI_CLK_P	RSVD
GND	GND	GND_27	79	80	GND_28	GND	GND
DP1_TXD3_N	DP1_TXD3_N	DP1_TXD3_N	81	82	DSI_D1_N	DSI_D1_N	RSVD
DP1_TXD3_P	DP1_TXD3_P	DP1_TXD3_P	83	84	DSI_D1_P	DSI_D1_P	RSVD
GND	GND	GND_29	85	86	GND_30	GND	GND
GPIO00	GPIO00	GPIO00	87	88	DP0_HPD	DP0_HPD	RSVD
SPI0_MOSI	SPI0_MOSI	SPI0_MOSI	89	90	DP0_AUX_N	DP0_AUX_N	RSVD
SPI0_SCK	SPI0_SCK	SPI0_SCK	91	92	DP0_AUX_P	DP0_AUX_P	RSVD
SPI0_MISO	SPI0_MISO	SPI0_MISO	93	94	HDMI_CEC	HDMI_CEC	HDMI_CEC
SPI0_CS0*	SPI0_CS0*	SPI0_CS0	95	96	DP1_HPD	DP1_HPD	DP1_HPD
SPI0_CS1*	SPI0_CS1*	SPI0_CS1	97	98	DP1_AUX_N	DP1_AUX_N	DP1_AUX_N
UART0_TXD	UART0_TXD	UART0_TXD	99	100	DP1_AUX_P	DP1_AUX_P	DP1_AUX_P
UART0_RXD	UART0_RXD	UART0_RXD	101	102	GND_31	GND	GND
UART0_RTS*	UART0_RTS*	UART0_RTS	103	104	SPI1_MOSI	SPI1_MOSI	SPI1_MOSI
UART0_CTS*	UART0_CTS*	UART0_CTS	105	106	SPI1_SCK	SPI1_SCK	SPI1_SCK
GND	GND	GND_32	107	108	SPI1_MISO	SPI1_MISO	SPI1_MISO
USB0_D_N	USB0_D_N	USB0_D_N	109	110	SPI1_CS0	SPI1_CS0*	SPI1_CS0*
USB0_D_P	USB0_D_P	USB0_D_P	111	112	I2S3_DIN	I2S3_DIN	SPI1_CS1*
GND	GND	GND_33	113	114	CAM0_PWDN	CAM0_PWDN	CAM0_PWDN
USB1_D_N	USB1_D_N	USB1_D_N	115	116	CAM0_MCLK	CAM0_MCLK	CAM0_MCLK
USB1_D_P	USB1_D_P	USB1_D_P	117	118	GPIO01	GPIO01	GPIO01 (CLK)
GND	GND	GND_34	119	120	CAM1_PWDN	CAM1_PWDN	CAM1_PWDN
USB2_D_N	USB2_D_N	USB2_D_N	121	122	CAM1_MCLK	CAM1_MCLK	CAM1_MCLK
USB2_D_P	USB2_D_P	USB2_D_P	123	124	I2S2_DOUT	I2S2_DOUT	GPIO02
GND	GND	GND_35	125	126	I2S2_DIN	I2S2_DIN	GPIO03
GPIO04	I2S2_FS	I2S2_FS	127	128	I2S2_SCLK	I2S2_SCLK	GPIO05
GND	GND	GND_36	129	130	I2S3_FS	I2S3_FS	GPIO06
PCIE0_RX0_N	PCIE0_RX0_N	PCIE0_RX0_N	131	132	GND_37	GND	GND
PCIE0_RX0_P	PCIE0_RX0_P	PCIE0_RX0_P	133	134	PCIE0_TX0_N	PCIE0_TX0_N	PCIE0_TX0_N
GND	GND	GND_38	135	136	PCIE0_TX0_P	PCIE0_TX0_P	PCIE0_TX0_P
PCIE0_RX1_N	PCIE0_RX1_N	PCIE0_RX1_N	137	138	GND_39	GND	GND
PCIE0_RX1_P	PCIE0_RX1_P	PCIE0_RX1_P	139	140	PCIE0_TX1_N	PCIE0_TX1_N	PCIE0_TX1_N
GND	GND	GND_40	141	142	PCIE0_TX1_P	PCIE0_TX1_P	PCIE0_TX1_P
CAN_RX	CAN_RX	CAN_RX	143	144	GND_41	GND	GND
CAN_TX	CAN_TX	CAN_TX	145	146	GND_42	GND	GND
GND	GND	GND_43	147	148	PCIE0_TX2_N	RSVD	PCIE0_TX2_N
PCIE0_RX2_N	RSVD	PCIE0_RX2_N	149	150	PCIE0_TX2_P	RSVD	PCIE0_TX2_P
PCIE0_RX2_P	RSVD	PCIE0_RX2_P	151	152	GND_44	GND	GND
GND	GND	GND_45	153	154	PCIE0_TX3_N	RSVD	PCIE0_TX3_N
PCIE0_RX3_N	RSVD	PCIE0_RX3_N	155	156	PCIE0_TX3_P	RSVD	PCIE0_TX3_P
PCIE0_RX3_P	RSVD	PCIE0_RX3_P	157	158	GND_46	GND	GND

GND	GND	GND_47	159	160	PCIE0_CLK_N	PCIE0_CLK_N	PCIE0_CLK_N
USBSS0_RX_N	USBSS_RX_N	USBSS_RX_N	161	162	PCIE0_CLK_P	PCIE0_CLK_P	PCIE0_CLK_P
USBSS0_RX_P	USBSS_RX_P	USBSS_RX_P	163	164	GND_48	GND	GND
GND	GND	GND_49	165	166	USBSS_TX_N	USBSS_TX_N	USBSS0_TX_N
PCIE1_RX0_N	PCIE1_RX0_N	PCIE1_RX0_N	167	168	USBSS_TX_P	USBSS_TX_P	USBSS0_TX_P
PCIE1_RX0_P	PCIE1_RX0_P	PCIE1_RX0_P	169	170	GND_50	GND	GND
GND	GND	GND_51	171	172	PCIE1_TX0_N	PCIE1_TX0_N	PCIE1_TX0_N
PCIE1_CLK_N	PCIE1_CLK_N	PCIE1_CLK_N	173	174	PCIE1_TX0_P	PCIE1_TX0_P	PCIE1_TX0_P
PCIE1_CLK_P	PCIE1_CLK_P	PCIE1_CLK_P	175	176	GND_52	GND	GND
GND	GND	GND_53	177	178	MOD_SLEEP	MOD_SLEEP*	MOD_SLEEP*
PCIE_WAKE*	PCIE_WAKE*	PCIE_WAKE	179	180	PCIE0_CLKREQ	PCIE0_CLKREQ*	PCIE0_CLKREQ*
PCIE0_RST*	PCIE0_RST*	PCIE0_RST	181	182	PCIE1_CLKREQ	PCIE1_CLKREQ*	PCIE1_CLKREQ*
PCIE1_RST*	PCIE1_RST*	PCIE1_RST	183	184	GBE_MDI0_N	GBE_MDI0_N	GBE_MDI0_N
I2C0_SCL	I2C0_SCL	I2C0_SCL	185	186	GBE_MDI0_P	GBE_MDI0_P	GBE_MDI0_P
I2C0_SDA	I2C0_SDA	I2C0_SDA	187	188	GBE_LED_LINK	GBE_LED_LINK	GBE_LED_LINK
I2C1_SCL	I2C1_SCL	I2C1_SCL	189	190	GBE_MDI1_N	GBE_MDI1_N	GBE_MDI1_N
I2C1_SDA	I2C1_SDA	I2C1_SDA	191	192	GBE_MDI1_P	GBE_MDI1_P	GBE_MDI1_P
I2S0_DOUT	I2S0_DOUT	I2S0_DOUT	193	194	GBE_LED_ACT	GBE_LED_ACT	GBE_LED_ACT
I2S0_DIN	I2S0_DIN	I2S0_DIN	195	196	GBE_MDI2_N	GBE_MDI2_N	GBE_MDI2_N
I2S0_FS	I2S0_FS	I2S0_FS	197	198	GBE_MDI2_P	GBE_MDI2_P	GBE_MDI2_P
I2S0_SCLK	I2S0_SCLK	I2S0_SCLK	199	200	GND_54	GND	GND
GND	GND	GND_55	201	202	GBE_MDI3_N	GBE_MDI3_N	GBE_MDI3_N
UART1_TXD	UART1_TXD	UART1_TXD	203	204	GBE_MDI3_P	GBE_MDI3_P	GBE_MDI3_P
UART1_RXD	UART1_RXD	UART1_RXD	205	206	GPIO07	GPIO07	GPIO07
UART1_RTS*	UART1_RTS*	UART1_RTS	207	208	GPIO08	GPIO08	GPIO08
UART1_CTS*	UART1_CTS*	UART1_CTS	209	210	CLK_32K_OUT	CLK_32K_OUT	CLK_32K_OUT
GPIO09	GPIO09	GPIO09	211	212	I2S3_SCLK	I2S3_SCLK	GPIO10
CAM_I2C_SCL	CAM_I2C_SCL	CAM_I2C_SCL	213	214	FORCE_RECOVERY	FORCE_RECOVERY*	FORCE_RECOVERY*
CAM_I2C_SDA	CAM_I2C_SDA	CAM_I2C_SDA	215	216	GPIO11	GPIO11	GPIO11
MODULE_ID	GND	GND_56	217	218	I2S3_DOUT	I2S3_DOUT	GPIO12
PCIE2_RST*	SDMMC_DAT0	SDMMC_DAT0	219	220	I2S1_DOUT	I2S1_DOUT	I2S1_DOUT
PCIE2_CLKREQ*	SDMMC_DAT1	SDMMC_DAT1	221	222	I2S1_DIN	I2S1_DIN	I2S1_DIN
PCIE3_RST*	SDMMC_DAT2	SDMMC_DAT2	223	224	I2S1_FS	I2S1_FS	I2S1_FS
PCIE3_CLKREQ*	SDMMC_DAT3	SDMMC_DAT3	225	226	I2S1_SCLK	I2S1_SCLK	I2S1_SCLK
PCIE3_CLK_N	SDMMC_CMD	SDMMC_CMD	227	228	GPIO13	GPIO13	GPIO13
PCIE3_CLK_P	SDMMC_CLK	SDMMC_CLK	229	230	GPIO14	GPIO14	GPIO14
GND	GND	GND_57	231	232	I2C2_SCL	I2C2_SCL	I2C2_SCL
SHUTDOWN_REQ*	SHUTDOWN_REQ*	SHUTDOWN_REQ	233	234	I2C2_SDA	I2C2_SDA	I2C2_SDA
PMIC_BBAT	PMIC_BBAT	PMIC_BBAT	235	236	UART2_TXD	UART2_TXD	UART2_TXD
POWER_EN	POWER_EN	POWER_EN	237	238	UART2_RXD	UART2_RXD	UART2_RXD
SYS_RESET*	SYS_RESET*	SYS_RESET	239	240	SLEEP/WAKE	SLEEP/WAKE*	SLEEP/WAKE*
GND	GND	GND_58	241	242	GND_59	GND	GND
GND	GND	GND_60	243	244	GND_61	GND	GND
GND	GND	GND_62	245	246	GND_63	GND	GND
GND	GND	GND_64	247	248	GND_65	GND	GND
GND	GND	GND_66	249	250	GND_67	GND	GND
VDD_IN	VDD_IN	VDD_IN_1	251	252	VDD_IN_2	VDD_IN	VDD_IN
VDD_IN	VDD_IN	VDD_IN_3	253	254	VDD_IN_4	VDD_IN	VDD_IN
VDD_IN	VDD_IN	VDD_IN_5	255	256	VDD_IN_6	VDD_IN	VDD_IN
VDD_IN	VDD_IN	VDD_IN_7	257	258	VDD_IN_8	VDD_IN	VDD_IN
VDD_IN	VDD_IN	VDD_IN_9	259	260	VDD_IN_10	VDD_IN	VDD_IN